

CUDA Teaching Center:

NVIDIA has selected the CS department to be one of its 2013-2014 CUDA Teaching Centers, committed to advancing the state of parallel GPU research and education using the CUDA C/C++ programming model. The department has received a number of CUDA-capable GPUs (one Tesla K20 GPUs and five GTX480s) and ten Massively Parallel Programming books for research and teaching purposes, plus \$12K for teaching assistance. The center is led by Zhiling Lan and Ioan Raicu, along with three Ph.D. students (Sean Wallace, Scott Krieder and Eduardo Berrocal).



We have constructed and managed a 19-node GPU-enabled cluster named Jarvis for systems courses including CS546, CS550, CS451, CS554, et

The Jarvis cluster

